

FIG. 1

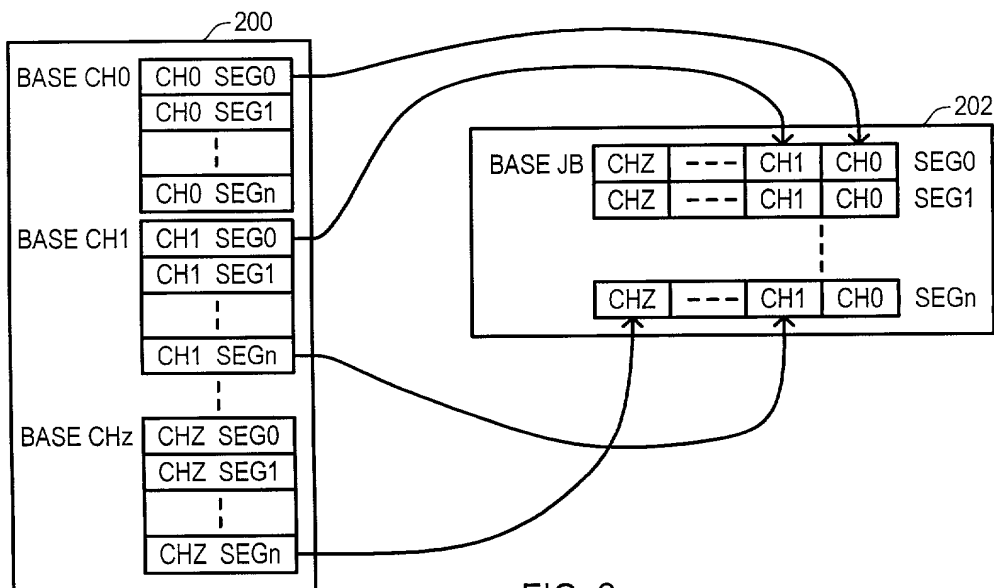


FIG. 2

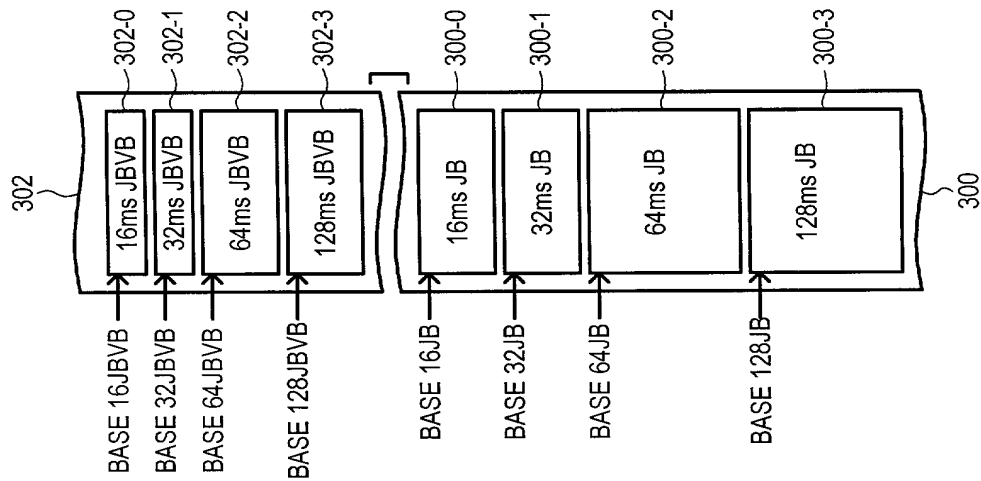


FIG. 3

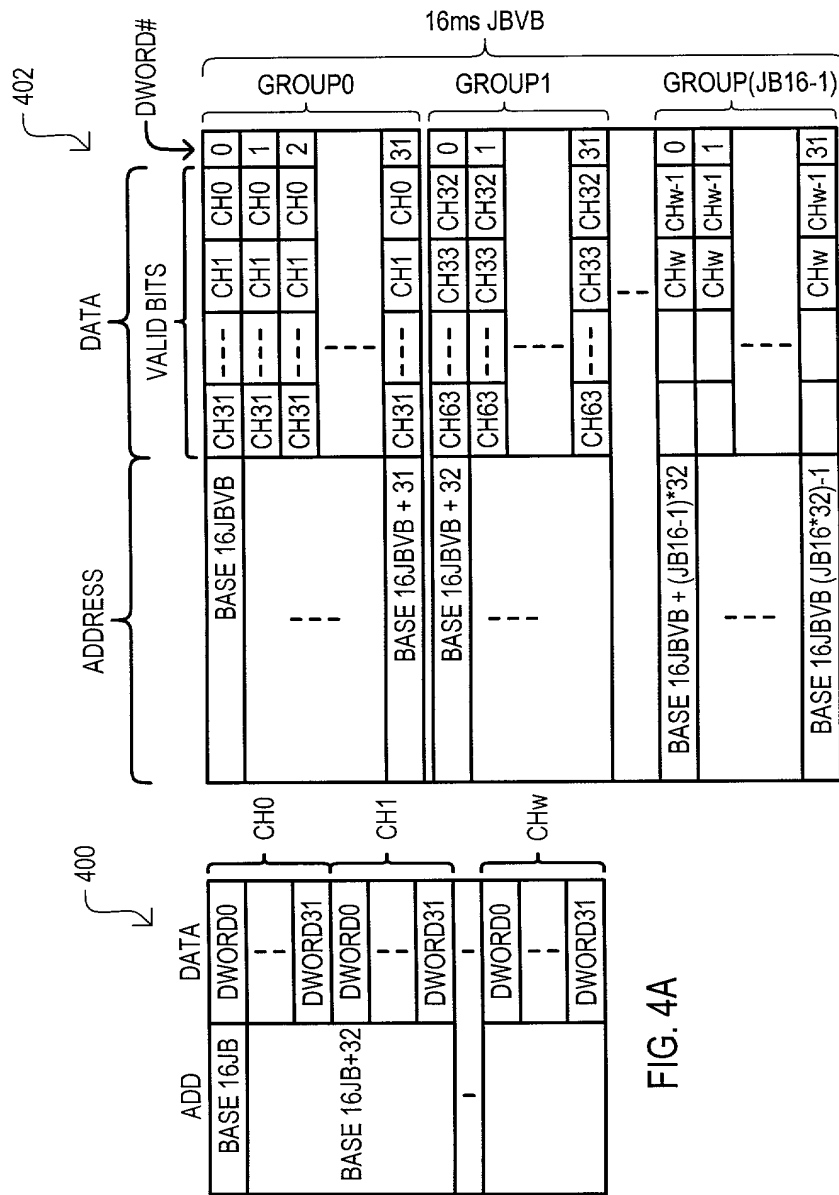


FIG. 4B

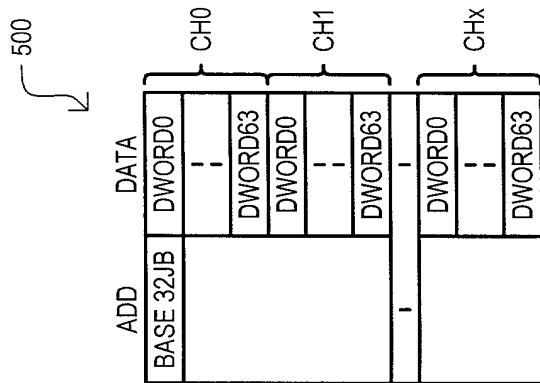


FIG. 5A

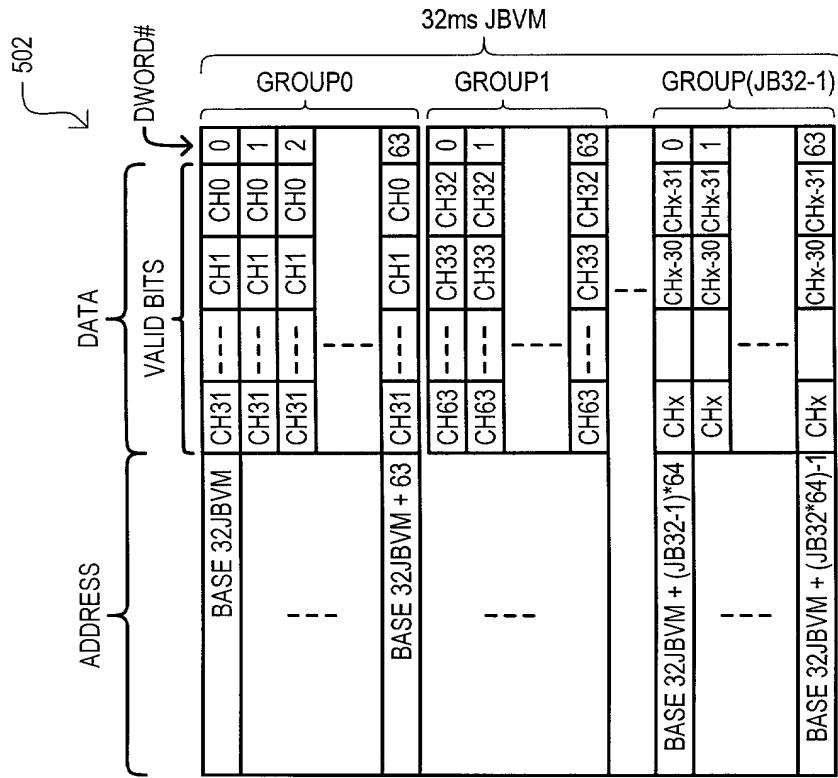


FIG. 5B

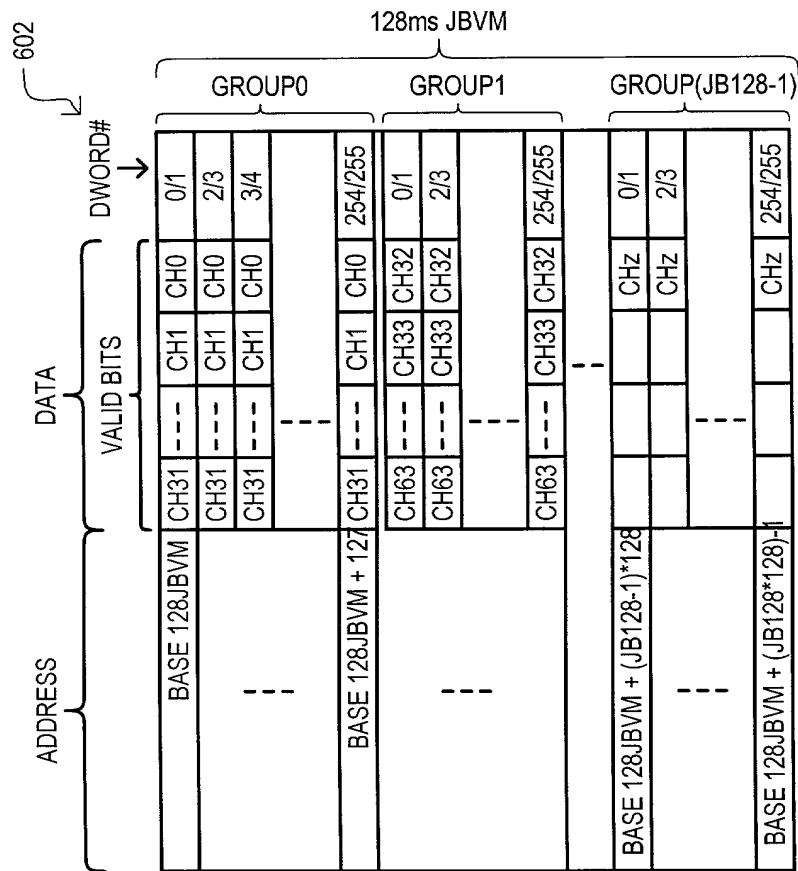


FIG. 6B

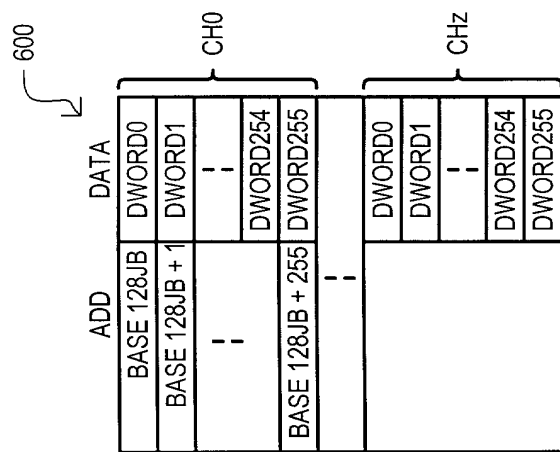


FIG. 6A

BUFFER SIZE	JB MEMORY ADDRESS	OFFSET	BIT SELECT
16ms	[16:12], [6:2]	BASE 16JBVB	[11:7]
32ms	[17:13], [7:2]	BASE 32JBVB	[12:8]
64ms	[18:14], [8:2]	BASE 64JBVB	[13:9]
128ms	[19:15], [9:3]	BASE 128JBVB	[14:10]

FIG. 7

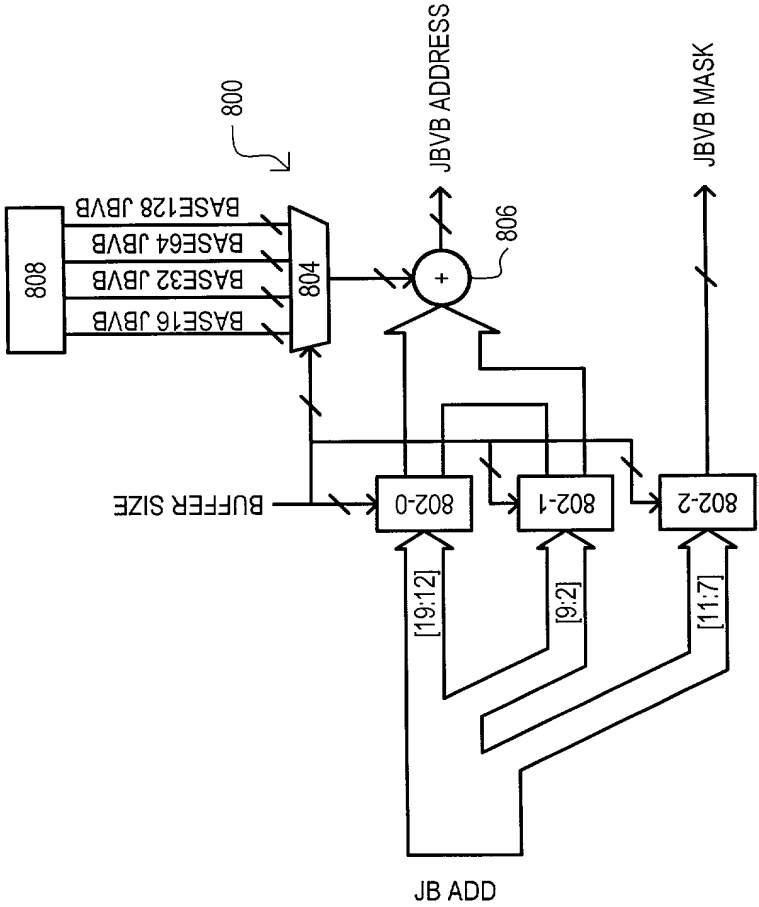


FIG. 8

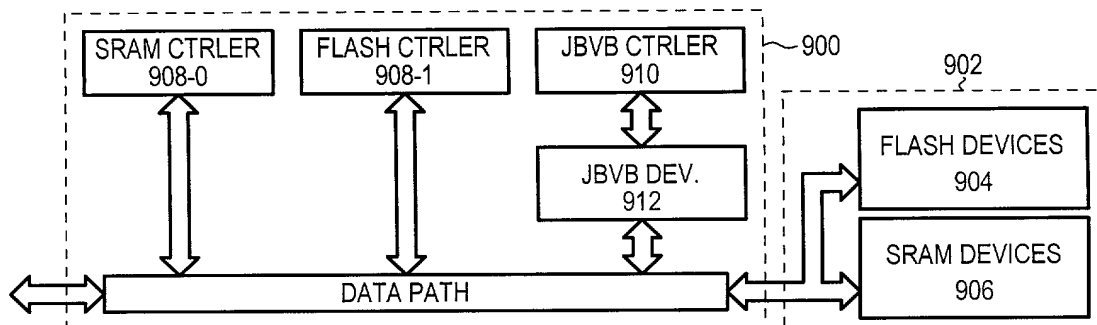


FIG. 9

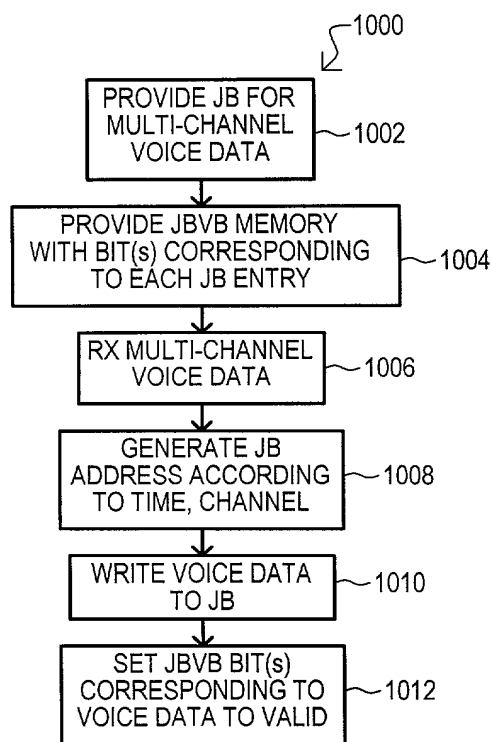


FIG. 10A

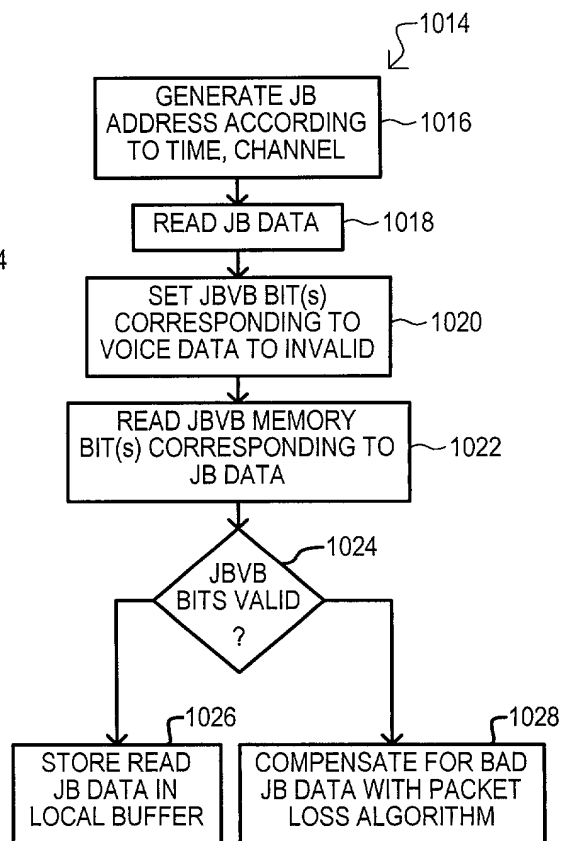


FIG. 10B

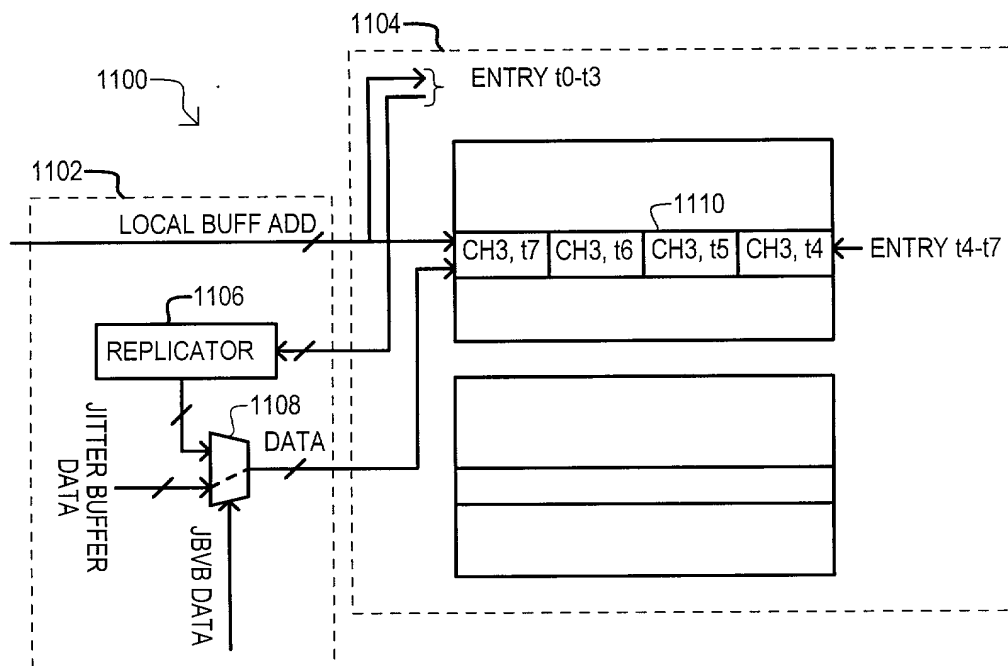


FIG. 11A

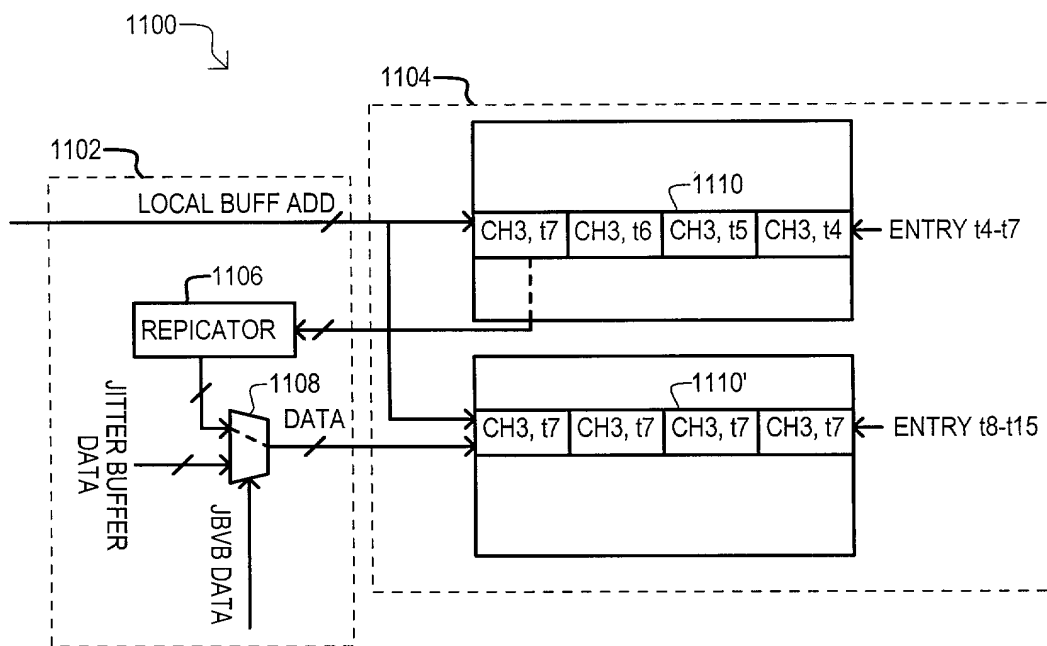


FIG. 11B